MMM	MMM	TTTTTTTTTTTTTT	ННН	HHH	RRRRRRRR	RRRR	TTTTTTTTTTTTTT	LLL
MMM	MMM	††††††††††††††††	ННН	ННН	RRRRRRRR		TTTTTTTTTTTTT	
MMM	MMM	ŤŤŤŤŤŤŤŤŤŤŤŤŤŤŤŤŤ	ННН	ннн	RRRRRRR		i i i i i i i i i i i i i i i i i i i	
MMMMMM	MMMMMM	111	ННН	ннн	RRR	RRR	777	
MMMMMM	MMMMMM	+++						FFF
		111	ННН	ннн	RRR	RRR	ŢŢŢ	ŕŕŕ
MMMMMM		!!!	ННН	HHH	RRR	RRR	ŢŢŢ	LLL
	MMM MMM	ŢŢŢ	ННН	HHH	RRR	RRR	TTT	LLL
	MMM MMM	111	HHH	HHH	RRR	RRR	TTT	LLL
MMM	MMM MMM	TTT	HHH	HHH	RRR	RRR	TTT	LLL
MMM	MMM	TTT	<b>НИНИНИНИНИ</b>		RRRRRRRR		ŤŤŤ	ĬĬĬ
MMM	MMM	TTT	<b>НИНИНИНИНИ</b>		RRRRRRRR		ŤŤŤ	<i>ו</i> ווֹ דּ
MMM	MMM	ŤŤŤ	<b>НИНИНИНИНИ</b>		RRRRRRRR		ŤŤŤ	iii
MMM	MMM	ŤŤŤ	ННН	ннн	RRR RR		ŤŤŤ	ili
MMM	MMM	ŤŤŤ	нин	ннн	RRR RR		ήii	
MMM	MMM	ή††	HHH	HHH	RRR RR		111	LLL
MMM		   T T						LLL
	MMM		ННН	ННН	RRR	RRR	ŢŢŢ	rrr
MMM	MMM	III	HHH	ННН	RRR	RRR	ŢŢŢ	LLL
MMM	MMM	TTT	ННН	HHH	RRR	RRR	TTT	LLL
MMM	MMM	TTT	HHH	HHH	RRR	RRR	TTT	
MMM	MMM	TTT	HHH	HHH	RRR	RRR	TTT	LLLLLLLLLLLLLL
MMM	MMM	111	ННН	HHH	RRR	RRR	ŤŤ	

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		\$			

OTS\$DIVCD - D COMPLEX+16 , D COMPLEX+16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 Page 0 Table of contents

(2) 45 HISTORY ; Detailed Current Edit History
(3) 53 DECLARATIONS
(4) 87 D COMPLEX+16 / D COMPLEX+16 giving D COMPLEX+16 result

075

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- D COMPLEX*16 / D COMPLEX*16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 6-SEP-1984 11:27:34 [MTHRTL.SRC]OTSDIVCD.MAR:1
                                                                                                                                        Page
                                                                                                                                                 (1)
       0000
                                    .TITLE OTS$DIVCD - D COMPLEX+16 / D COMPLEX+16 DIVISION ROUTINE
                                    .IDENT /1-001/
                                                                       : File: OTSDIVCD.MAR
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                             COPYRIGHT (c) 1978, 1980, 1982, 1984 BY DIGITAL EQUIPMENT CORPORATION, MAYNARD, MASSACHUSETTS.
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                       : FACILITY: MATH LIBRARY
       0000
                   30
       0000
                   31 ; ABSTRACT:
                   32 ;
33 ;
       0000
       0000
                                   Perform D COMPLEX*16 division
       0000
                   35 ;--
       0000
       0000
                   36 :
       0000
                   37
                          AUTHOR:
       0000
                   38
                                   Steven B. Lionel, 12-July-1979
       0000
                   39
                          MODIFIED BY:
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                   40
       0000
                   41
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- D COMPLEX*16 / D COMPLEX*16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 Page 2 HISTORY; Detailed Current Edit History 6-SEP-1984 11:27:34 [MTHRTL.SRCJOTSDIVCD.MAR;1 (2) 0000 45 .SBTTL HISTORY; Detailed Current Edit History 0000 46 0000 47 0000 48; Edit History 0000 49; 0000 50 0000 50 0000 51; 1-001 - Adapted from OTS$DIVC version 1-003. SBL 12-July-1979
```

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none

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3 (3)

Page

(4)

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- D COMPLEX+16 / D COMPLEX+16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 D COMPLEX+16 / D COMPLEX+16 g ving D COM 6-SEP-1984 11:27:34 [MTHRTL.SRC]OTSDIVCD.MAR;1
                             .SBTil D COMPLEX+16 / D COMPLEX+16 giving D COMPLEX+16 result
      0000
      0000
                89
      0000
                    : FUNCTIONAL DESCRIPTION:
      0000
                91
                93
93
      0000
                             OTS$DIVCD_R3 - D COMPLEX*16 / D COMPLEX*16 giving D COMPLEX*16 result
      0000
      0000
      0000
                95
                             The COMPLEX*16 result is computed as follows:
      0000
      0000
                97
                             1) Let (a, b) represent the COMPLEX*16 dividend.
      0000
                                Let (c, d) represent the COMPLEX+16 divisor.
                             3) Let (r, i) represent the COMPLEX+16 quotient.
      0000
0000
0000
                99
               100
               101
                             Then:
              102
      0000
      0000
                             r = (ac + bd) / (cc + dd)
      0000
               104
                             i = (bc - ad) / (cc + dd)
      0000
               105
      0000
               106
                     CALLING SEQUENCE:
      0000
              107
      0000
               108
                             Complex_quotient.wdc.w = OTS$DIVCD_R3(dividend.rdc.v, divisor.rdc.v)
      0000
               109
      0000
                      INPUT PARAMETERS:
              110
      0000
               111
              112
                             Dividend and divisor parameters are represented as FORTRAN D COMPLEX*16 numbers and are CALL BY VALUE.
      0000
      0000
      0000
              114
                             Passing 128 bit quantities by value is a violation
              115
      0000
                             of the VAX calling standard, but is excused because
      0000
              116
                             this is a code support routine not meant to be
              117
      0000
                             callable by users.
      0000
              118
      0000
              119
                     IMPLICIT INPUTS:
      0000
              120
                             NONE
              121
122
123
124
125
126
127
128
129
130
      0000
      0000
                      OUTPUT PARAMETERS:
      0000
                             NONE
      0000
      0000
                      IMPLICIT OUTPUTS:
      0000
                             NONE
      0000
      0000
                     FUNCTIONAL VALUE:
      0000
      0000
                             The D COMPLEX*16 value returned is (a, b) / (c, d) in resisters RO-R3! This is a violation of the VAX
      0000
      0000
                             calling standard, but is excused because this is
      0000
                             a code support routine, not meant to be callable
      0000
                             by users.
      0000
              136
137
      0000
                     SIDE EFFECTS:
      0000
              138
139
      0000
                             Modifies registers RO-R3!
                             SS$ ROPRAND if either argument is a reserved operand. SS$_FLTOVF if floating overflow
      0000
      0000
              140
      0000
              141
                             SSS_FLTDIV if divide by zero
```

142 :--

Page

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- D COMPLEX+16 / D COMPLEX+16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 D COMPLEX+16 / D COMPLEX+16 giving D COM 6-SEP-1984 11:27:34 [MTHRTL.SRC]OTSDIVCD.MAR;1
                                                                                                                                                                (5)
                                             144
                                                              .ENTRY OTS$DIVCD_R3, ^M<R4,R5,R6,R7,R8,R9,R10,R11>
                                    $000
$000
$000
$000
$000
                                                             MTHSFLAG_JACKET
                                                                                                        : establish math error handler
            00000000 GF
                              9E
                                                             MOVAB
                                                                        G^MTH$$JACKET_HND, (FP)
                                    0009
                                                                                                         ; set handler address to jacket
                                    0009
                                                                                                         ; handler
                                    0009
                                    0009
                                    0009
                                             147
                                                     Perform scaling of all operands before division
                                    0009
                                             148
                                    0009
                                             149
      14 AC
                                                                                                        ; R1 = c(AP) < exp > 0,1,...377
; R0 = d(AP) < exp > 0,1,...377
                                                                        #7, #8, c(AP), R1
#7, #8, d(AP), R0
                                                              EXTZV
                              EF
B1
14
                 08
51
50
                        07
50
51
50
7
51
      1C AC
                                    000F
                                             150
                                                              EXTZV
                                                                        RO, R1
                                    0015
                                             151
                                                              CMPW
                                                                                                         ; RO = MAX (c < exp>, d < exp>)
                                             152
                                    0018
                                                                        2$
                                                              BGTR
                              B0
8E
90
                 50
                                    001A
                                                              MOVW
                                                                        RO, RO
#7, RO, RO
                                             154 2$:
                 50
                                    001D
                                                              MNEGB
                                                                                                         ; R0 = scaling exponent 0,377,376,...,1
                 50
          50
                                    0020
                                                              ROTL
                                                                                                         : build a floating scale factor
                                             156
157
                              D4
                                    0024
                                                              CLRL
                                    0026
                                                                                                         ; scale all operands
                        50
50
50
50
      58
5A
54
             14 AC
                               65
                                    0026
                                             158
                                                                        RO, c(AP), R8
RO, d(AP), R10
                                                                                                         ; R8-R9 gets c
; R10-R11 gets d
                                                              MULD3
                              65
             1C AC
                                    005B
                                             159
                                                              MULD3
             04 AC
                                    0030
                                             160
                                                             MULD3
                                                                        RO, a(AP), R4
             OC AC
                              65
                                    0035
                                             161
                                                                        RO. b(AP), R2
                                                             MULD3
                                                                                                         ; b
                                             162
                                    003A
                              65
65
          50
56
                                                                        R4, R8, R0
R2, R10, R6
                        54256A854
                                    003A
                                                             MULD3
                                                                                                           R0 = ac
                                                             MULD3
                                    003E
                                             164
                                                                                                           R7 = bd
                 50
52
52
58
58
58
                                                             ADDD2
                                    0042
                                                                        R6, R0
R10, R4
                              50
                                             165
                                                                                                           R0 = ac+bd
                              64
64
62
                                                             MULD2
                                             166
                                                                                                           R4 = ad
                                                                        R8, R2
R4, R2
R8, R8
R10, R10
                                                                                                           R2 = bc
R2 = bc - ad
                                    0048
                                             167
                                    004B
                                             168
                                                             SUBD2
                       58
58
58
58
                                                                                                        ; R8 = cc
                              64
                                    004E
                                             169
                                                             MULD2
                                             170
171
172
173
174
175
                              64
                                    0051
                                                             MULDZ
                                                                                                           R10 = dd
                                    0054
                              60
                                                             ADDD2
                                                                        R10, R8
                                                                                                        : R8 = cc + dd
                                    0057
                                                                        R8, R0
R8, R2
                              66
                                                             DIVDS
                                                                                                           R0 = (ac+bd) / (cc+dd)
                        58
                                    005A
                              66
                                                             DIVD2
                                                                                                        : R2 = (bc-ad) / (cc+dd)
                                    005D
                              04
                                    005D
                                                             RET
                                                                                                        ; (R0-R1, R2-R3) = (r, i)
```

005E

005E

176 177

.END

```
- D COMPLEX*16 / D COMPLEX*16 DIVISION R 16-SEP-1984 01:53:20 VAX/VMS Macro V04-00 6-SEP-1984 11:27:34 [MTHRTL.SRC]OTSDIVCD.MAR;1
OTS$DIVCD
                                                                                                                                Page
                                                                                                                                       (5)
Symbol table
                = 00000004
                 B
                =
                I
                = 0000001C
MTHSSJACKET HND
                  *******
OTSSDIVCD_R3
                  00000000 RG
                                                    Psect synopsis!
PSECT name
                                 Allocation
                                                      PSECT No.
                                                                  Attributes
  ABS
                                 00000000 (
                                                      00 ( 0.)
                                                                  NOPIC
                                                                          USR
                                                                                       ABS
                                                                                             LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
                                                                                CON
OTSSCODE
                                                94.)
                                 0000005E (
                                                      01 ( 1.)
                                                                    PIC
                                                                          USR
                                                                                CON
                                                                                       REL
                                                                                             LCL
                                                                                                   SHR
                                                                                                         EXE
                                                                                                               RD NOWRT NOVEC LONG
                                                 Performance indicators !
Phase
                          Page faults
                                          CPU Time
                                                          Elapsed Time
                                 29
121
Initialization
                                          00:00:00.08
                                                          00:00:01.21
Command processing
                                          00:00:00.65
                                                          00:00:04.02
                                          00:00:00.67
Pass 1
                                  83
                                                          00:00:03.71
Symbol table sort
                                          00:00:00.00
                                                          00:00:00.00
                                          00:00:00.55
Pass 2
                                                          00:00:02.32
Symbol table output
                                          00:00:00.01
                                                          00:00:00.04
Psect synopsis output
                                                         00:00:00.11
                                          00:00:00.02
Cross-reference output
                                          00:00:00.00
                                                          00:00:00.00
Assembler run totals
                                 286
                                          00:00:01.98
                                                          00:00:11.50
The working set limit was 900 pages.
3071 bytes (6 pages) of virtual memory were used to buffer the intermediate code.
There were 10 pages of symbol table space allocated to hold 6 non-local and 1 local symbols.
237 source lines were read in Pass 1, producing 11 object records in Pass 2.
1 page of virtual memory was used to define 1 macro.
                                                Macro library statistics !
Macro library name
                                               Macros defined
                                                          0
_$255$DUA28:[SYSLIB]STARLET.MLB;2
```

O GETS were required to define O macros.

There were no errors, warnings or information messages.

MACRO/ENABLE=SUPPRESSION/DISABLE=(GLOBAL, TRACEBACK)/LIS=LIS\$:OTSDIVCD/OBJ=OBJ\$:OTSDIVCD MSRC\$:MTHJACKET/UPDATE=(ENH\$:MTHJACKET)+MSRC

0264 AH-BT13A-SE

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